

Tips When Using Cray MPI

















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- Introduction to Cray MPI
- Tips and useful environment variables for Cray systems with Intel Xeon and Xeon-phi processors
- Hybrid MPI + OpenMP applications

General Recommendations

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Brief Introduction to Cray MPI

- CRAY
- I/O, collectives, P2P, and one-sided all optimized for Cray system architecture
 - SMP-aware collectives
 - High performance single-copy on-node communication via xpmem (not necessary to program for shared memory)
- Highly tunable through environment variables
 - Defaults should generally be best, but some cases benefit from fine tuning
- Integrated within the Cray Programming Environment
 - Compiler drivers manage compile flags and linking automatically
 - Profiling through Cray performance tools



Cray MPI Documentation



Primary user resource for tuning and feature documentation is the man page

• man intro_mpi

OR

• man MPI

Standard function documentation available as well

• E.g., man mpi_isend

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MPI Rank Reorder – Two Interfaces Available

• CrayPat

- Available with sampling or tracing
- Include –g mpi when instrumenting program
- Run program and let CrayPat determine if communication is dominant, detect communication pattern and suggest MPI rank order if applicable

grid_order utility

- User knows communication pattern in application and wants to quickly create a new MPI rank placement file
- Available when perftools-base module is loaded

MPI Rank Order Observations



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MPI Rank Order Observations (2)

MPI Grid Detection:

There appears to be point-to-point MPI communication in a 96 X 8 grid pattern. The 52% of the total execution time spent in MPI functions might be reduced with a rank order that maximizes communication between ranks on the same node. The effect of several rank orders is estimated below.

A file named MPICH_RANK_ORDER.Grid was generated along with this report and contains usage instructions and the Custom rank order from the following table.

Rank Order	On-Node Bytes/PE	On-Node Bytes/PE% of Total Bytes/PE	MPICH_RANK_REORDER_METHON
Custom	2.385e+09	95.55 %	3
SMP	1.880e+09	75.30%	1
Fold	1.373e+06	0.06%	2
RoundRobin	0.000e+00	0.00%	0

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Auto-Generated MPI Rank Order File

in this file targets nodes 3,491,49,387,89,451,121,483 2,530,34,562,66,538,98,522,10, 3,236,465,204,473,244,393,188, 545,297,633,361,625,321,58	5.53
with multi-core	
6,436,102,468,70,404,38,412,14 570,42,554,26,594,50,602 7,601,289,553,353,593,521,	569,
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Msg Total Bytes collected for: 86,396,30,428,62,460,54,492,11 6,634,90,578,114,618,122,610 5,172,417,180,449,148,489,220, 256,373,261,341,264,349,28	30,31
# 8,420,22,452,94,388,126,484 135 215 167 239 199 347 259 30 7,272,381,269,309,285,333,	277,
# Program: 129,563,193,531,161,571,225,537,231,371,239,379,191,331,247,131,534,195,542,163,566,227,52,365	
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demo/Rank/sweep3d/src/sweep3d 153,587,169,627,137,635,201,611,207,275,183,283,151,267,215,590,211,630,179,638,139,62,344	
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111,397,63,461,55,429,87,421,2 157,510,189,462,173,430,205,39 0,734,662,686,670,726,702,	694,
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104,556,16,628,80,636,56,620,4 9,176,637,144,621,208,581,216, 8,623,92,551,116,583,124,615 722,731,763,658,642,755,739,67,759	645,
8,516,112,580,88,548,120,612 3,440,35,432,67,400,99,408,11,5,707,650,682,715,689,666,690,	
1 403 65 435 33 411 97 443 9 4 5,439,37,407,69,447,101,415,13 464,43,496,27,472,51,504 747	
67,25,499,105,507,41,475,475,471,45,503,29,479,77,511 19,392,75,424,59,456,83,384,10,257,345,265,313,281,305,273,33	
53,399,85,431,21,463,61,391,107,416,91,488,115,448,123,480 7,609,369,577,377,617,329,513,	

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MPICH_RANK_REORDER_METHOD

- Vary your rank placement to optimize communication
- Can be a quick, low-hassle way to improve performance
- Use CrayPAT to produce a specific MPICH_RANK_ORDER file to maximize intra-node communication
- Or, use grid_order utility with your application's grid dimensions to layout MPI ranks in alignment with data grid

• To use:

- name your custom rank order file: MPICH_RANK_ORDER
- export MPICH_RANK_REORDER_METHOD=3

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MPI Rank Reorder (continued)

- A topology and placement-aware reordering method is also available (uses node allocation information)
- Optimizes rank ordering for Cartesian decompositions using the layout of nodes in the job

• To use:

- user@login> export MPICH_RANK_REORDER_METHOD=4
- user@login> export MPICH_RANK_REORDER_OPTS=\

"-ndims=3 -dims=16,16,8"

See intro_mpi(1) man page for more information

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HUGEPAGES

- Linking and running with hugepages can offer a significant performance improvement for many MPI communication sequences, including MPI collectives and basic MPI_Send / MPI_Recv calls
- Most important for applications calling MPI_Alltoall[v] or performing point-to-point operations with a similarly well connected pattern
- To use HUGEPAGES, load desired module at link and run time:
 - module load craype-hugepages8M (many sizes supported)
 - << re-link your app >>
 - module load craype-hugepages8M
 - << run your app >>

Using DMAPP

- DMAPP optimizations not enabled by default because...
 - May reduce resources MPICH has available (shared with DMAPP)
 - Requires more memory (for DMAPP internals)
 - DMAPP does not handle transient network errors
- These are highly-optimized algorithms which may result in significant performance gains, but user has to request them

Supported DMAPP-optimized functions

- MPI_Allreduce (4-8 bytes)
- MPI_Bcast (4 or 8 bytes)
- MPI_Barrier
- MPI_Put / MPI_Get / MPI_Accumulate

• To use, link with libdmapp and set the following environment variable

- Collective use: user@login> export MPICH_USE_DMAPP_COLL=1
- RMA one-sided use: user@login> export MPICH_RMA_OVER_DMAPP=1

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MPICH GNI Environment Variables

Used to optimize inter-node traffic using the Aries interconnect, the following are the most significant variables to try (avoid significant deviations from the default if possible):

• MPICH_GNI_MAX_VSHORT_MSG_SIZE

Controls max message size for E0 mailbox path (Default: varies)

• MPICH_GNI_MAX_EAGER_MSG_SIZE

Controls max message size for E1 Eager Path (Default: 8K bytes)

MPICH_GNI_NUM_BUFS

Controls number of 32KB internal buffers for E1 path (Default: 64)

• MPICH_GNI_NDREG_MAXSIZE

Controls max message size for R0 Rendezvous Path (Default: 4MB)

MPICH_GNI_RDMA_THRESHOLD

• Controls threshold for switching to BTE from FMA (Default: 1K bytes)

See the MPI man page for further details

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Specific Collective Algorithm Tuning

- Different algorithms may be used for different message sizes in collectives (e.g.)
 - Algorithm A might be used for Alltoall for messages < 1K
 - Algorithm B might be used for messages >= 1K
- To optimize a collective, you can modify the cutoff points when different algorithms are used, which may improve performance
- MPICH_ALLGATHER_VSHORT_MSG
- MPICH_ALLGATHERV_VSHORT_MSG
- MPICH_GATHERV_SHORT_MSG
- MPICH_SCATTERV_SHORT_MSG
- MPICH_GNI_A2A_BLK_SIZE
- MPICH_GNI_A2A_BTE_THRESHOLD
- MPICH_MAX_THREAD_SAFETY=multiple (for thread multiple support)

See the MPI man page for further details

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Using MPI + OpenMP

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MPI Thread Multiple Support

• Thread multiple support for

- point to point operations (optimized global lock)
- Collectives (optimized global lock)
- MPI-RMA (thread hot)

All supported in default library

- user@login> export MPICH_MAX_THREAD_SAFETY=multiple
- Global lock optimization on by default (N/A for MPI-RMA)
 - 50% better 8B latency than pthread_mutex() (OSU latency_mt, 32 threads per node, Broadwell)
 - export MPICH_OPT_THREAD_SYNC=0 falls back to pthread_mutex()

Thread Hot Communication

"Thread hot": high performance thread multiple support

• Design Objectives

- Contention Free progress and completion
- High bandwidth and high message rate
- Independent progress thread(s) flush outstanding traffic, other threads make uninterrupted progress
- Dynamic mapping between threads and network resources
- Locks needed only if the number of threads exceed the number of network resources

MPI-3 RMA

- Epoch calls (Win_complete, Win_fence) are thread-safe, but not intended to be thread hot
- All other RMA calls (including request-based operations) are thread hot
- Multiple threads doing Passive Synchronization operations likely to perform best

Multi-threading Optimizations in Cray MPI

- Easy way to hit the ground running on a KNL MPI only mode
 - Works quite well in our experience
 - Scaling to more than 2-8 threads most likely requires a different application design approach
- "Bottom-Up" OpenMP development approach
- "Top-Down" SPMD model
 - Increases the scope of code executed by OpenMP, allows for better load balancing and overall compute scaling on KNL
 - Allows multiple threads to call MPI concurrently
 - In this model, performance is limited by the level of support offered by MPI for multi-threaded communication
 - MPI implementations must offer "Thread-Hot" communication capabilities to improve communication performance for highly threaded use cases on KNL



Examples of MPI + OpenMP Use

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"bottom up"

! Keep OpenMP within a "compute" loop

DO WHILE (t .LT. tend)

DO patch = 1, npatches

CALL update_patch()

...CALL MPI...

END DO

END DO

SUBROUTINE update_patch()

!\$OMP PARALLEL DO DO i = 1, nx ...do work... END DO

END SUBROUTINE

"SPMD"

! Move OpenMP near the top of the call stack

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!#OMP PARALLEL DO WHILE (t .LT. tend)

!#OMP DO DO patch = 1, npatches

CALL update_patch()

...CALL MPI...

END DO

END DO

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Recommendations

- Using hugepages in MCDRAM can improve large message communication performance
- Using thread multiple with Cray's Thread-Hot capabilities on Intel Xeon and Intel KNL architectures is a key tool for hybrid applications
- Using asynchronous communication can hide/overlap communication overheads and improve application scalability
- MPI-only works quite well on KNL
 - Threading can be helpful, but unless SPMD with "thread-hot" MPI is used scaling to more than 2-8 threads not recommended
- Collectives implemented with user pt2pt is strongly discouraged
 - Especially for alltoall, bcast, and gather
 - Very unlikely pt2pt will perform better
 - If they do, please file a bug with Cray



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