We will talk about only

- Compilations for Cori (Haswell and KNL nodes) and Edison
- Compile/link lines
  
  ```
  Compiler +
  Compiler Flags +
  -l/path/to/headers +
  -L/path/to/library -l<library>
  ```

- Available libraries, linking examples

- Users will need to apply the above info to their own build systems.
Cori Haswell, KNL and Edison Configurations

- Cori KNL and Haswell – a Cray XC40
  - Cori has 9688 single-socket Intel® Xeon Phi™ Processor 7250 ("Knights Landing") nodes @1.4 GHz with 68 cores (272 threads) per node, two 512 bit vector units per core, and 16 GB high bandwidth on-package memory (MCDRAM) with 5X the bandwidth of DDR4 DRAM memory (>400 GB/sec) and 96 GB DDR4 2400 MHz memory per node.
  - In addition, Cori has 2388 dual-socket 16-core Intel® Xeon™ Processor E5-2698 v3 ("Haswell") nodes @2.3GHz with 32 cores (64 threads) per node, two 256 bit vector units per core, 128 GB 2133 MHz DDR4 memory.
  - Cori nodes are interconnected with Cray’s Aries network with Dragonfly topology.

- Edison – a Cray XC30
  - Edison has 5586 dual-socket 12-core Intel(R) Xeon(R) CPU E5-2695 v2 ("Ivy Bridge") nodes @2.40GHz with 24 cores (48 threads) per node, two 256 bit vector units per core, 64 GB DDR3 1866 MHz memory. Edison nodes are interconnected with Cray’s Aries network with Dragonfly topology.
Compilations on Cori and Edison are very similar

- Three programming environments are supported
  - Intel, GNU and Cray compilers are available on Cori. **Intel** is the default.
  - PrgEnv-intel, PrgEnv-gnu, and PrgEnv-cray loads the corresponding programming environment which includes the compilers and matching libraries.
  - Using module swap PrgEnv-Intel PrgEnv-cray to swap programming environment.
  - Compiler wrappers, ftn, cc and CC, are recommended instead of the native compiler invocations.
Compilations on Cori and Edison are very similar

- Default environment loads `craype-haswell/craype-ivybridge` module on Cori/Edison, which sets the env `CRAY_CPU_TARGET` to haswell/ivybridge for Cori/Edison – taking care of the architectural difference when compiling with the compiler wrappers.

- Cross compilation: compiling for compute nodes from login nodes

Default programming environment on Cori:

```
zz217@cori05:-->$ module list
Currently Loaded Modulefiles:
  1) modules/3.2.10.6
  2) nsp/1.2.0
  3) intel/18.0.1.163
  4) craype-network-aries
  5) craype/2.5.12
  6) cray-lbsci/17.09.1
  7) udebug/2.3.2-6.0.4.0-12.2__g2f9c3ee.ari
  8) ugni/6.0.14-6.0.4.0-14.1__ge7db4a2.ari
  9) pmi/5.0.12
 10) dmapp/7.1.1-6.0.4.0-46.2__gb8abda2.ari
 11) gni-headers/5.0.11-6.0.4.0-7.2__g7136988.ari
 12) xpmem/2.2.2-6.0.4.1-18.2__g43b0535.ari
 13) job/2.2.2-6.0.4.0-8.2__g3c644b5.ari
 14) dvs/2.7.2.2.36-6.0.4.1-16.2__g4c8274a
 15) alps/6.4.1-6.0.4.0-7.2__g86d0f3d.ari
 16) rca/2.2.15-6.0.4.1-13.1__g46acb0f.ari
17) atm/2.1.1
18) ProEnv-intel/6.0.4
19) craype-haswell
20) cray-mpich/7.6.2
21) altld/2.0
22) darshan/3.1.4
```
Compilations on Cori and Edison are very similar

To compile for Cori Haswell and Edison:

- **To use Intel compiler:**
  - `ftn -O3 mycode.f90. # Fortran:`
  - `cc -O3 mycode.c # for C`
  - `CC -O3 myC++code.C. # for C++`

- **To use GNU compiler**
  - `module swap PrgEnv-intel PrgEnv-gnu`
  - `ftn -O3 mycode.f90. # Fortran:`
  - `cc -O3 mycode.c # for C`
  - `CC -O3 myC++code.C. # for C++`

- Note, the compiler wrappers invoke the Intel, GNU, or Cray compilers under the hood.
To compile for Cori KNL

- Unlike Edison, Cori has two types of compute nodes, Haswell, and KNL, and applications are compiled for both Haswell and KNL nodes from the login nodes (Haswell nodes).
- do module swap craype-haswell craype-mic-knl before compiling for Cori KNL nodes

```bash
module swap craype-haswell craype-mic-knl
ftn -O3 mycode.f90.       # Fortran:
cc -O3 mycode.c          # for C
CC -O3 myC++code.C.     # for C++
```
**Binary compatibility**

<table>
<thead>
<tr>
<th>Build system</th>
<th>Edison</th>
<th>Cori Haswell</th>
<th>Cori KNL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Edison</td>
<td>✓</td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Cori Haswell</td>
<td></td>
<td>✓</td>
<td>✓</td>
</tr>
<tr>
<td>Cori KNL</td>
<td></td>
<td></td>
<td>✓</td>
</tr>
</tbody>
</table>

- Edison binaries runs on Cori Haswell, and KNL; Haswell Binaries run on KNL
- We recommend a separate build for each platform for optimal performance.
Compiler recommendations

● Will not recommend any specific compiler
  ○ Intel - better chance of getting processor specific optimizations, especially for KNL
  ○ Cray compiler – many new features and optimizations, especially with Fortran; useful tools like reveal work with Cray compiler only
  ○ GNU - widely used by open software

● Start with the compilers that vendor/code developers used so to minimize the chance to hit the compiler and code bugs, then explore different compilers for optimal performance.
### Compiler flags

<table>
<thead>
<tr>
<th>Intel</th>
<th>GNU</th>
<th>Cray</th>
<th>Description/ Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>-O2</td>
<td>-O0</td>
<td>-O2</td>
<td>default</td>
</tr>
<tr>
<td>default, or –O3</td>
<td>-O2 or -O3,-Ofast</td>
<td>default</td>
<td>recommended</td>
</tr>
<tr>
<td>-qopenmp</td>
<td>-fopenmp</td>
<td>default, or –h omp</td>
<td>OpenMP</td>
</tr>
<tr>
<td>-g</td>
<td>-g</td>
<td>-g</td>
<td>debug</td>
</tr>
<tr>
<td>-v</td>
<td>-v</td>
<td>-v</td>
<td>verbose</td>
</tr>
</tbody>
</table>

- **Validity check after compilation**
- **Compilers’ default behavior could vary between compilers**
  - Default number of OpenMP threads used is all CPU slots available for Intel and GNU compilers; 1 for Cray compiler.
Compiler wrappers, ftn, cc and CC, are recommended

- Use ftn, cc, and CC to compile Fortran, C and C++ codes, respectively, instead of the underlying native compilers, such as ifort, icc, icpc, gfortran, gcc, g++, etc.
  - The compiler wrappers wraps the underlying compilers with the additional compiler and linker flags depending on the modules loaded in the environment
  - The same compiler wrapper command (e.g. ftn) is used to invoke any compilers supported on the system (Intel, GNU, Cray)

- Compiler wrappers do cross compilation
  - Compiling on login nodes to run on compute nodes
Compiler wrappers, ftn, cc and CC, are recommended

- Use the –host=x86_64 configure option when compiling for KNL from a login node
- To compile on a KNL node, do `salloc –N 1 –q interactive –C knl –t 4:00:00` to get on a compute node

- Compilers wrappers link statically by default
  - Preferred for performance at scale

- Use –dynamic or set an environment variable `CRAYPE_LINK_TYPE=dynamic` to link dynamically
  - A dynamically linked executable may take a some time to load shared libraries when running with a large number of processes

- User provided options take precedence
Why compiler wrappers?

- They include the architecture specific compiler flags into the compilation/link line automatically.

- Automatically add the header and library paths and libraries on the compilation/link lines
  - Compiler wrappers use the pkg-config tools to dynamically detect paths and libs from the environment (working with cray modules and some NERSC modules)
  - The architecture specific builds of libraries will be linked into

<table>
<thead>
<tr>
<th></th>
<th>Intel*)</th>
<th>GNU</th>
<th>Cray</th>
<th>Module</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cori KNL</td>
<td>-xMIC-AVX512</td>
<td>-march=knl</td>
<td>-h cpu=mic-knl</td>
<td>craype-mic-knl</td>
</tr>
<tr>
<td>Cori Haswell</td>
<td>-xCORE-AVX2</td>
<td>-march=core-avx2</td>
<td>-h cpu=haswell</td>
<td>craype-haswell</td>
</tr>
<tr>
<td>Edison Ivy Bridge</td>
<td>-xCORE-AVX-I</td>
<td>-march=corei7-avx</td>
<td>-h cpu=ivybridge</td>
<td>craype-ivybridge</td>
</tr>
</tbody>
</table>

*) for the latest Intel compilers, -march=knl,haswell,ivybridge can be used instead of –xcode.
What do compiler wrappers link by default?

- Depending on the modules loaded, compiler wrappers link to the MPI, LAPACK/BLAS/ScALAPACK libraries, and more automatically.

- Library names could be different from what you used before.

```bash
$ module list
Currently Loaded Modulefiles:
1) modules/3.2.18.6
2) nas/2.1.0
3) intel/18.0.1.163
4) craype-network-aries
5) craype/2.5.12
6) cray-libsci/17.09.1
7) udreg/2.3.2-6.0.4.0.12.2_g2f9c3ee.ar
8) ugni/6.8.14-6.0.4.0.14.1_ge7db4a2.ar
9) pm1i/3.0.12
10) dmapp/7.1.1-6.8.4.0.462_gb89dab2.ar
11) header/18.5.11-6.8.4.0.7.2-g7136988.ar
12) xpmem/2.2.2-6.0.4.1.18.2_g43b835.ar
13) job/2.2.2-6.0.4.0.8.2_g3645a85.ar
14) dvs/2.2.2.3-6.8.4.1.16.2_g4c8274a.ar
15) alps/6.4.1-6.0.4.0.7.2_g860f3d.ar
16) rca/2.2.15-6.0.4.1.13.1_g46ac8bf.ar
```

- The module list shows the system has loaded various modules, which are then linked by default.

- The library names and versions might differ from what you were used to previously.

- For example, the library `udreg/2.3.2-6.0.4.0.12.2_g2f9c3ee.ar` is linked by default.

- The `module list` command is a useful tool for checking what modules are currently loaded and the libraries they link to.
More on the verbose output from compiler wrappers

Note, -Wl,--start-group … -Wl,--end-group for static linking
Available libraries

- Cray supports many software packages – Cray Developer Toolkits (CDT)
  - Access via modules, type “module avail” or “module avail –S” to see the available modules
  - There are different builds for different compilers
  - Programming environment modules allow the libraries built with the matching compilers to be linked to

- NERSC also supports many libraries
  - Some of them interact with the Cray compiler wrappers while many of them do not.

- Where are the libraries?
  - Use “module show <module name> “ to see the installation paths
  - `ls –l <installation_path>` to see the library files
Examples of linking to the Cray provided libraries

- Linking to Cray MPI and Cray Scientific libraries are automatic by default if compiler wrappers are used
  
  CC parallel_hello.cpp or ftn dgemmx1.f90

- Linking to HDF5 and NETCDF libraries are automatic, user just need to load the cray-hdf5 or cray-netcdf modules
  
  module load cray-hdf5; cc h5write.c
  ○ Note The library name could be different. Using the –v option to see the library names and other detailed link line information.
Examples of linking to the Cray provided libraries

- Linking to PETSc libraries are automatic, but users need to choose a proper module (real/complex, 32/64 bit integer)
  - E.g., module load cray-petsc-complex-64
  - Use cc –v test1.c to see the linking detail

- Linking to fftw libraries – fftw 3 is the default
  - module load cray-fftw
  - Loading the cray-fftw module always links to the pthread version of the library, -lfftw3f_mpi -lfftw3f_threads -lfftw3 -lfftw3_mpi -lfftw3_threads -lfftw3, to link with OpenMP implementation, need to manually provide the libraries.
Examples of linking to the NERSC provided library modules

● Some of the NERSC provided modulefiles are written to interact with the Cray compiler wrappers, e.g., elpa module on Cori

    module load elpa
    ftn –qopenmp –v test2.f90   # this will automatically link to elpa and MKL ScaLAPACK libraries

    ○ Type module show <module name> to check if the envs <libname>_PKGCONFIG_LIBS, PE_PKGCONFIG_PRODUCTS, and PKG_CONFIG_PATH are defined in the modulefile, which compiler wrappers look for.

● Most of the NERSC provided modulefiles do not interact with the compiler wrappers, user need to provide the include path and library path and libraries manually, e.g. GSL

    module load gsl; ftn test3.f90 $GSL

    ○ GSL is set as -I/usr/common/software/gsl/2.1/intel/include
      -L/usr/common/software/gsl/2.1/intel/lib -lgsl -lgslcblas
Linking to Intel MKL library

● Resource:
  ○ Learn from Intel compiler verbose output, -mkl={parallel,sequential,cluster}

● For intel compiler, use –mkl flag
  ○ ftn test1.f90 –mkl # default to parallel –multi-threaded lib
  ○ The loaded cray-libsci will be ignored if –mkl is used.
Linking to Intel MKL library

- For GNU compiler (e.g., to link to 32-bit integer build):
  - Save the MKLROOT from the Intel compiler module, and then
  - Threaded: -L$MKLROOT/lib/intel64 -Wl,--start-group -lmkl_gf_lp64 -lmkl_gnu_thread -lmkl_core -liomp5 -Wl,--end-group -lpthread -lm -ldl
  - ScaLAPACK: -L$MKLROOT/lib/intel64 -Wl,--start-group -lmkl_gf_lp64 -lmkl_gnu_thread -lmkl_scalapack_lp64 -lmkl_blacs_intelmpi_lp64 -lmkl_core -Wl,--end-group -lgomp -lpthread -lm -ldl
  - Note that mkl modules could be out-dated
Cray MPICH libraries are recommended for performance especially at scale.

Compiler wrappers links to Cray MPICH libraries.

However, if you need to link to Intel MPI library, do

```
module load impi
mpiifort test1.f90
```

- Note that the binaries linked to the Intel MPI need to run with `srun` instead of `mpirun` to get a proper process/thread affinity, [http://www.nersc.gov/users/computational-systems/cori/running-jobs/advanced-running-jobs-options/#toc-anchor-6](http://www.nersc.gov/users/computational-systems/cori/running-jobs/advanced-running-jobs-options/#toc-anchor-6)

- Native Intel compilers link dynamically
Summary

● Compilations for Cori and Edison are very similar

● To compile for Cori KNL, do
  - module swap craype-haswell craype-mic-knl

● Use compiler wrappers where possible, they
  ○ add architecture specific optimization flags
  ○ link to the Cray MPI and LibSci libraries and other Cray provided libraries

● Use available libraries where possible
  ○ Use module avail command to check available libraries
  ○ Use module show <module name> to see the installation paths if needed

● Learn from the compiler verbose output (-v)
Recommended readings

● NERSC website, especially,
  ○ http://www.nersc.gov/users/computational-systems/cori/programming/compiling-codes-on-cori/
  ○ http://www.nersc.gov/users/computational-systems/edison/programming/
  ○ We are moving user documentation pages to http://docs.nersc.gov,
  ○ https://docs.nersc.gov/development/compilers/
  ○ For further compiler optimizations read intel slides: e.g.,

● Compiler and linker man pages:
  ○ ifort, icc, icpc, crayftn, etc
  ○ man ld (-WI, -zmuldefs, -WI, -y<symbol>)
Thank You!
Building your application separately for each platform could be important to get optimal performance.

VASP built with the –xMIC-AVX512 flag runs 35% faster than built with the –xCORE-AVX2 flag on Cori KNL.