



IXPUG 2016
THE INTEL® XEON PHI™ USERS GROUP

Annual Meeting of the Intel Xeon Phi Users Group

September 19-22, 2016

Argonne National Laboratory



Optimization of scientific applications on Intel Xeon Phi
based HPC systems

Free exchange of information and ideas

Meetings and activities open to everyone

Independent

Strong support from, and interactions with, Intel

10 major events in ~2 years!



IT4Innovations
national
supercomputing
center



Richard Gerber, NERSC / Berkeley Lab
Thomas Steinke, Zuse Institut Berlin
Melyssa Fratkin, TACC
Janine Cook, Sanda
Doug Doerfler, NERSC / Berkeley Lab
Michael Lysaght, Irish Center for High-End Computing
David Martin, ALCF / Argonne National Lab
Kent Milfeld, TACC
Hai Ah Nam, Los Alamos National Lab
John Pennycook, Intel
Estela Suarez, Jülich Supercomputing Center
Florian Wende, Zuse Institut Berlin

Program Committee



David Martin, ANL

Richard Gerber, NERSC

Thomas Steinke, ZIB

Gilles Civario, Dell

Ryan Coleman, Sandia

Estela Suarez, Juelich

Janine Cook, Sandia

Doug Doerfler, NERSC

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Steve Leak, NERSC

Michael Lysaght, ICHEC

Georg Zitzlsberger, Intel

Kent Milfeld, TACC

John Pennycook, Intel

Abhinav Sarje, LBNL

Lisa Smith, Intel



Original
Xeon Phi

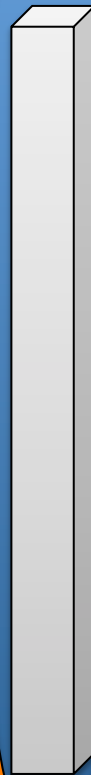
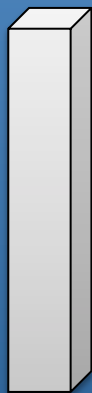
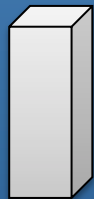
Original
Haswell

Optimized
Haswell

Workload
KNL Target

Optimized
Xeon Phi

IXPUG
Rock Stars





Schedule



Monday	Tutorials ✓
Tuesday	Plenary Talks by HPC centers on Deployment and Early Science, Keynotes by Rick Stevens (Argonne) and Joe Curley (Intel)
Wednesday	Technical Talks on Applications, Memory Models and Tuning, Tutorials on Optimization
Thursday	Technical Talks on Tools, Techniques and Vis, Keynote by Jim Tullos (Intel)



Optimizing Performance on Many-Core Processors: Unleashing the Power of the Intel® Xeon Phi and Beyond

Birds of a Feather

Wednesday, Nov. 16

12:15 – 1:15

Intel HPC Developer Conference

Nov. 12-13, 2016

Salt Lake City





Working Groups

www.ixpug.org/working-groups

General Optimization and
Tuning

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Vectorization

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MPI

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Thanks again!

Argonne Leadership Computing Facility

David Martin, Argonne

Intel

Lisa Smith, Intel

IXPUG Steering & Program Committees

Joe Curley, Intel

Melyssa Fratkin, TACC



David Martin



Melyssa Fratkin