



**ISC** High Performance  
The HPC Event.



# Unleashing the Power of Next-Generation Many-Core Processors

Richard A. Gerber (NERSC)  
Chris Gottbrath (Rogue Wave Software)  
Chris J. Newburn (Intel)  
Thomas Steinke (ZIB)

*Frankfurt, July 15<sup>th</sup> 2015*



## IXPUG WORKSHOP

# The Road to Application Performance on Intel Xeon Phi

Thursday, July 16, 2015

09:00 am - 01:00 pm

Room: Kilobyte, Frankfurt Marriott Hotel

[www.ixpug.org](http://www.ixpug.org)



# Agenda IXPUG BoF

12:15 **Welcome and Introduction** (Thomas Steinke, ZIB)

12:20 – 12:45 **Lightning Talks** (Chair: Chris Newburn, Intel)

- **Performance Optimization of OpenFOAM on Xeon/Xeon Phi**  
Nishant Agrawal, TCS Innovation Labs
- **Quickly selecting nearest particles in the FoF component of Gadget**  
Luigi Iapichino, Leibniz-Rechenzentrum (LRZ)
- **Migrating to OpenMP Offloading**  
Kent Milfeld & Carlos Rosales, Texas Advanced Computing Center
- **Offloading of the NWChem CCSD(T) Method at Scale**  
Michael Klemm, Intel GmbH
- **High Performance Python Offloading**  
Michael Klemm, Intel GmbH

12:45 – 12:55 **Tools Talks** (Michael Steyer, Intel and Mark O'Connor, Allinea)

12:55 – 13:05 **Open Discussion, Q&A**

13:05 – 13:10 **IXPUG Community and News** (Richard Gerber, IXPUG President)

13:10 – 13:15 **Wrap up** (Kent Milfield, TACC)